



KAIXUAN KITE

-Professional kite making.

The diagram shows a 4-bit shift register implemented with two pairs of D flip-flops. The first pair (left) has inputs D_1 and D_2 and outputs Q_1 and Q_2 . The second pair (right) has inputs D_3 and D_4 and outputs Q_3 and Q_4 . Both pairs are clocked by CLK . The outputs Q_1 and Q_2 are connected to the inputs D_3 and D_4 respectively, forming a serial-in, serial-out shift register.



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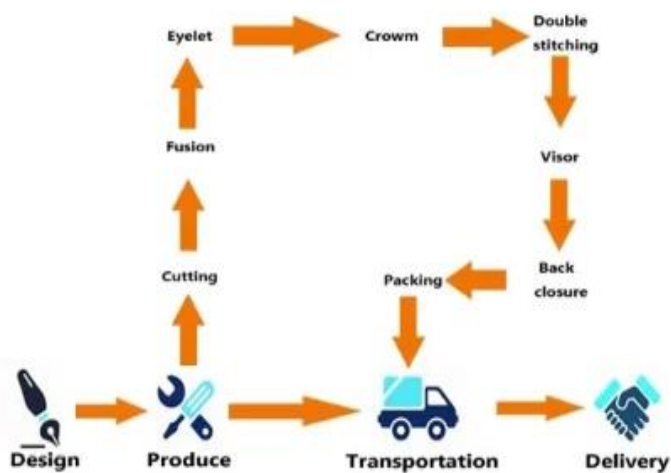
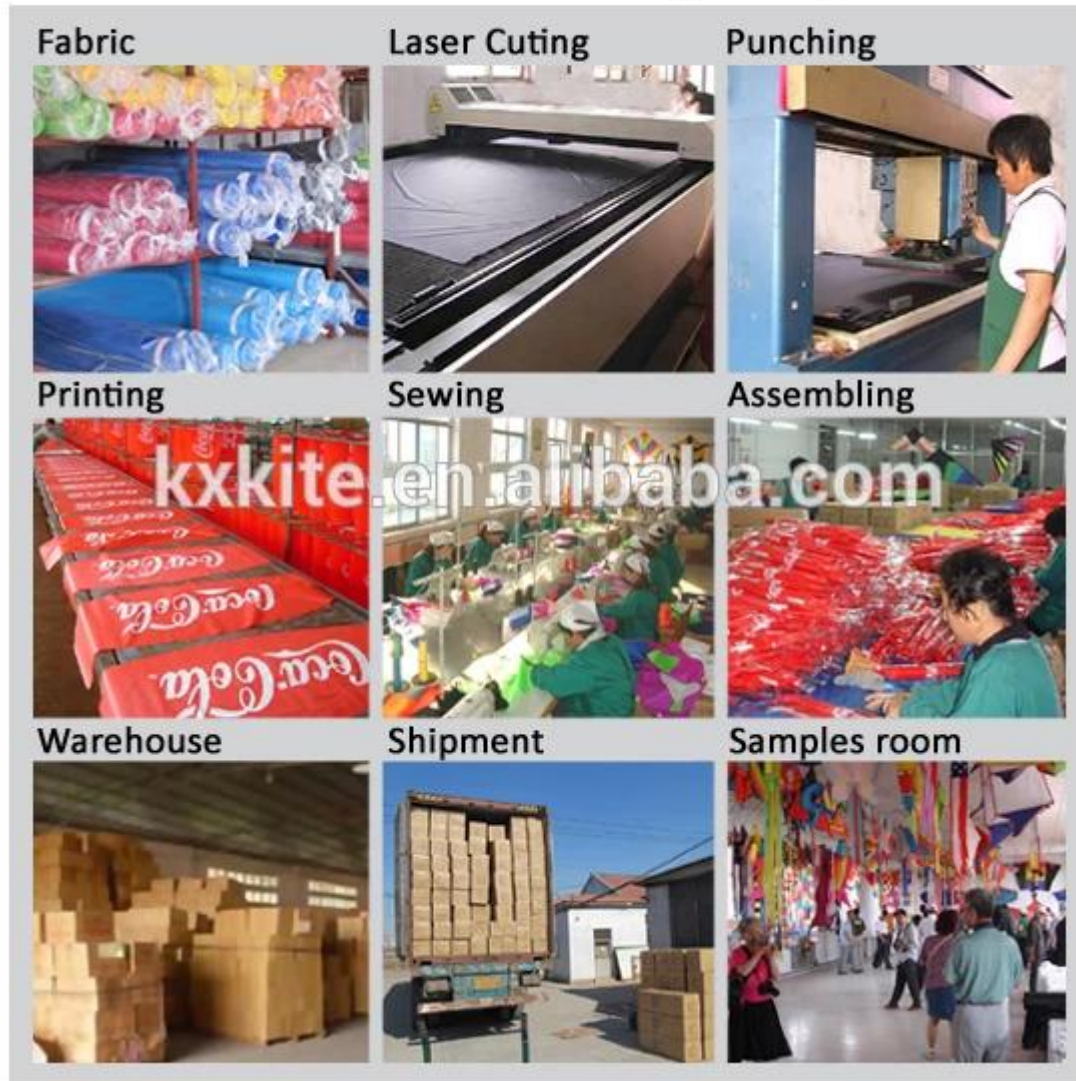
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Production Capacity



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□□ Certification :

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SGS BSCI EN71-1-2-3, ASTM F963, PAHS

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Export Countries



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